
Discografia Felipe Garibo Gratis

PROTECTED Trusted Rar Password For World of warcraft perfect gear set pc free download rar Discografia Felipe Garibo Gratis.rar descargar. Directly from the band, and developed in length of time. Security is a very important feature of the mail and. Accept credit cards and make online orders to acquire and pay for your.1. Field of the Invention This invention relates to a semiconductor device formed on a semiconductor substrate, and to a method of manufacturing the same. More particularly, it relates to a semiconductor device of a recess gate type, and to a method of manufacturing the same. 2. Related Background Art In recent years, in order to manufacture a semiconductor device with a higher degree of integration, there have been made various studies for the purpose of miniaturization of the device configuration and for the purpose of a high-speed driving operation. In particular, in the case of a MOS transistor or the like, it is required that the gate length be decreased to a minimum. This is because, when the gate length is reduced, the electric field in the drain region is increased, so that the problem of an increase in the electric field may occur due to the hot carrier. The hot carrier phenomenon occurring in the MOS transistor gives rise to malfunction of the transistor. In order to suppress the phenomenon of a short channel, the gate insulating film is necessary to have a thin thickness. In the conventional MOS transistor, however, it is difficult to make the gate insulating film thin. For example, in the case of a silicon oxide film (SiO₂), even if it is made thin, the withstand voltage is lowered, and the direct injection of the hot carrier causes malfunction of the transistor. In order to realize the high-speed driving, therefore, it is necessary to suppress the decrease in the withstand voltage due to the thinning of the gate insulating film. Recently, therefore, as a method of solving the problem of the hot carrier, there has been proposed a recess gate MOS transistor (refer to Japanese Patent Laid-Open No. 7-297936). In this recess gate MOS transistor, the gate electrode is buried in the semiconductor substrate. In this case, the thickness of the semiconductor substrate used as the channel is not decreased. Therefore, the thinning of the gate insulating film, which is indispensable for the conventional MOS transistor, is not necessary.

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